

ABSTRACT:

A data-processing system comprises
a microprocessor,

a communication device communicating with an electronic module intended
to apply a convention signal to the microprocessor, and a hardware circuit allowing inversion
5 or no inversion of the order of bits of a word as a function of the value of the convention
signal during transfer of the word between the electronic module and the microprocessor.

Such a system allows a time gain during the operation of converting the bits of
a word as a function of the convention used, which is beneficial for the real-time constraint.
The system concerned may be used, for example, in a terminal using one or more cards of the
10 SIM type, such as a mobile phone.

Fig. 1